### V SRI DYUTHI

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# **Career Objectives:**

To acquire a challenging career and to utilize my innovative and introspective skills to the best of my abilities in a firm where I can contribute my leadership ability and hard work to fulfil the objective of the organization.

### **Academia:**

Board / University	Year of Pass	Percentage %
Sreenidhi Institute of Science and Technology	2020	74.21
Sri Siddhartha Institute of Technology	2015	76.54
Sri Chaithanya Mahila Kalashala	2011	83.50
Jawahar Navodaya Vidyalaya	2009	75.46
	Sreenidhi Institute of Science and Technology Sri Siddhartha Institute of Technology Sri Chaithanya Mahila Kalashala	Sreenidhi Institute of Science and Technology  2020  Sri Siddhartha Institute of Technology  2015  Sri Chaithanya Mahila Kalashala  2011

## **Technical skills:**

Programming Languages	C, JAVA, Data Science(learning)
Operating Systems	Windows, Linux (Basic Operations)
Database Management	SQL, Unions, Joins
Hardware	Networking,

#### **Workshops:**

- Attended 'Conference on LTE and 5G Technologies in March 2019 at SNIST Deemed University'
- Attended 'FPGA Tool Learning and RTL Schematic at SNIST on JAN 2019'
- Workshop on the Wireless Sensor Networks, presented the survey topic on" Data Integration Networks ".
- Attended 'VLSI Cadence Systems Conference on Nov 2019

### **Academic Projects:**

<u>Project Name (B. Tech):</u> Traffic Sign Board Detection using Antenna Tx and Rx

<u>Description:</u> A Sign board detection system that could provide a prior intimation to the driver about the obstacles present in the roads such as steep curve, bends, bridges, temporary work on progress etc. and different zones to avoid accidents. Thus, the resulting damage can be minimized and several lives can be saved from the accidents

Status: Completed

<u>Project Name (M. Tech):</u> Highly Efficient Deep Learning Hardware Accelerator <u>Description:</u> The deep learning network measure systems that turns out to be progressively huge scale because of the requests of the reasonable applications. The architecture can be arranged to work various sizes of tile information to use the exchange offs among speedup and equipment costs.

Status: paper published in SCOPUS journal (IEEE format)

### Languages Known:

Language	Read	Write	Speak
Telugu	Y	Y	Y
Kannada	Y	Y	Y
English	Y	Y	Y
Hindi	Y	Y	Y

# **Area of Interest:**

Playing Shuttle Collecting different type of stones Dancing and Handy crafts

## **Skills:**

Self-motivation under stress conditions. Leadership Skills. Good Communication Skills

### **Personal Details:**

Date of Birth: 23rd Jan 1994

Gender: Female Nationality: Indian

Address 10-138, Brahman Wada, Asifabad, Telangana 504293

Place: Hyderabad Name: (V SRI DYUTHI)